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		Application Number	10/572,196
		Filing Date	September 21, 2006
		First Named Inventor	Herz et al.
		Art Unit	2811
		Examiner Name	Hung K. Vu
Sheet 1	of 1	Attorney Docket Number	1031-03

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	WESTE et al., CMOS Processing Technology, Principles of CMOS VLSI Design, A Systems Perspective, 1985, pp. 64-67, Addison-Wesley Publishing Company.	
	2	WESTE et al., CMOS Processing Technology, Principles of CMOS VLSI Design, A Systems Perspective, 1993, pp. 109-113, Addison-Wesley Publishing Company.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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